FIG. 1

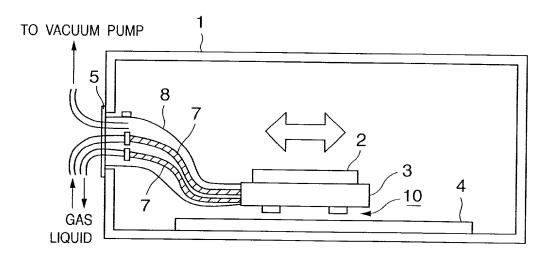


FIG. 2

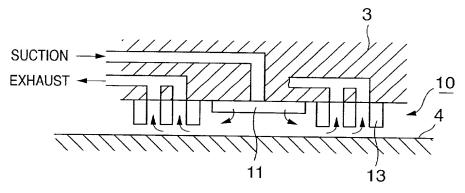
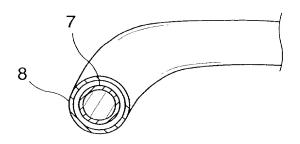
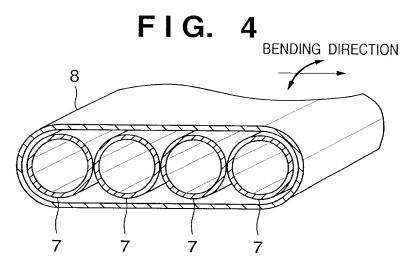
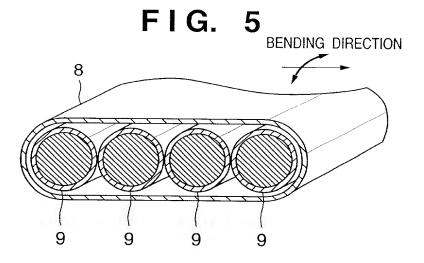


FIG. 3







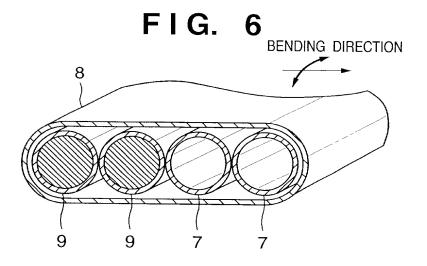


FIG. 7

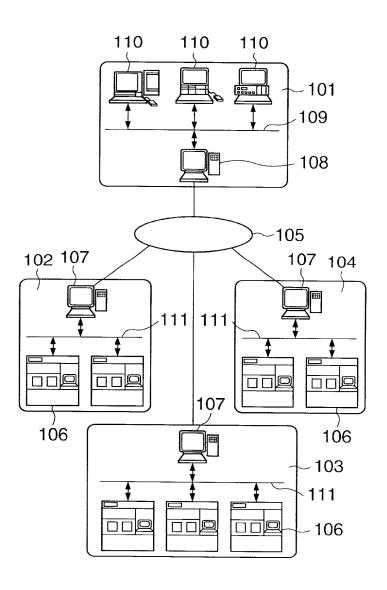


FIG. 8

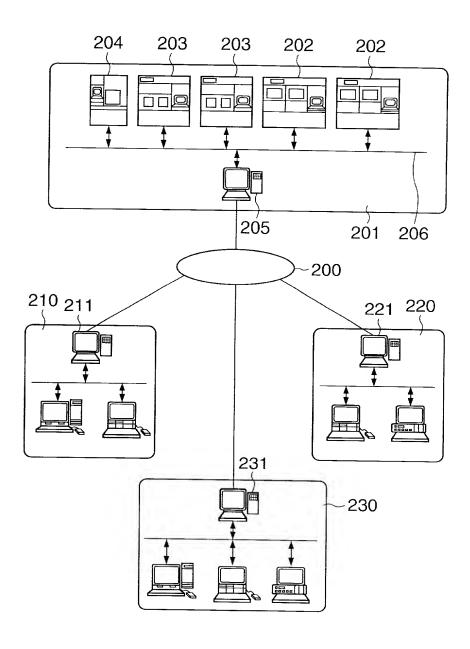


FIG. 9

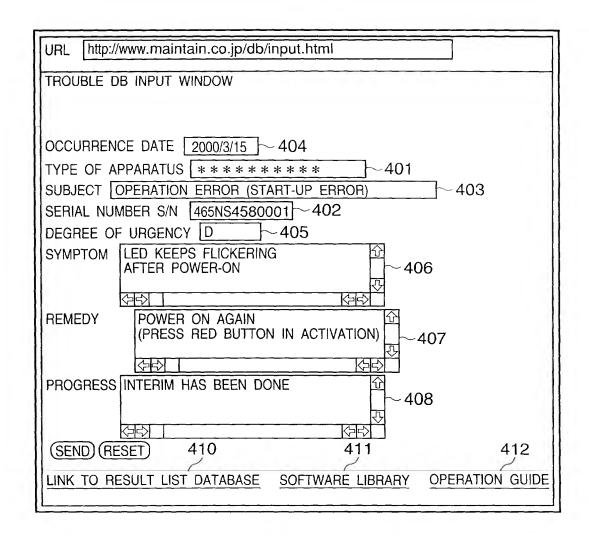
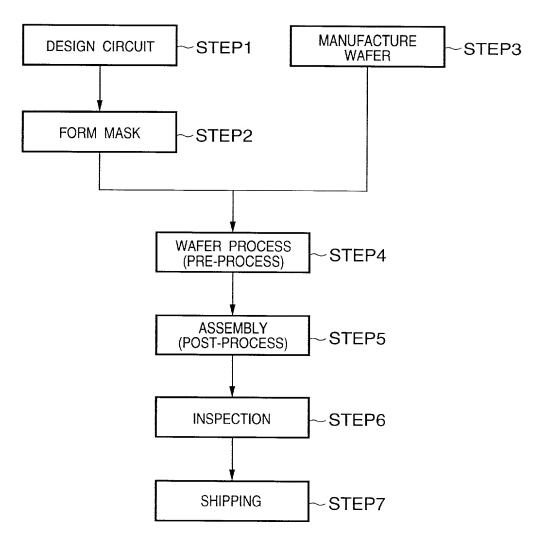
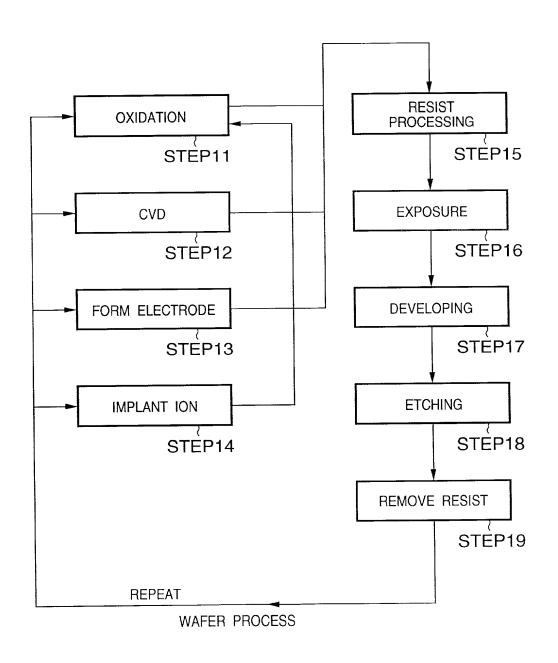


FIG. 10



SEMICONDUCTOR DEVICE MANUFACTURING FLOW

FIG. 11



ngm pang pina suga sa pang sa pang pina pang pang sa p

FIG. 12

